ASYL+/PLS

Schematic Entries
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Several silicon vendors like Xilinx or Actel provide you with symbol libraries and netlisting tools to have, together with the schematic capture software as OrCAD, ViewLogic, DataIO, etc., a powerful tool for design entering. It has been verified that the netlists (XNF, EDIF) are compatible with the PLS netlist optimization and migration facilities.

Please, contact MINC France support for more details.